

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of
Kazuhiro Nakajima

Serial No.: 10/649,833

Filed: August 28, 2003

For: TEST METHOD AND APPARATUS FOR VERIFYING
FABRICATION OF TRANSISTORS IN AN INTEGRATED CIRCUIT

Honorable Commissioner of Patents
Alexandria, VA 22313-1450



Group Art Unit: 2817

Examiner: Unknown

INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicant's duty of disclosure under 37 CFR §1.56, applicant respectfully brings the following documents, cited in the European Search Report and listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. Copies of the listed documents are provided herewith for the convenience of the Examiner.

In compliance with the concise explanation requirement under 37 CFR §1.98(a)(3), for foreign language documents, Applicant encloses herewith a copy of the European Search Report citing such documents, together with an English-language version (if not already included) of that portion of the Search Report indicating the degree of relevance found by the foreign office.

This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicant is aware.

I hereby certify that each item of information contained in this Information Disclosure Statement was the first citation of that item by a foreign patent office in a counterpart foreign application, which occurred not more than three months prior to the filing of this statement.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Please charge any deficiencies in fees and credit any overpayment of fees to Attorney's Deposit Account No. 50-0481.

Respectfully submitted,

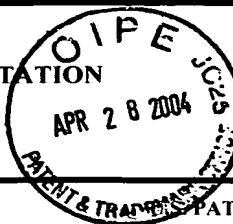
Sean M. McGinn

Registration No.: 34,386

Date: 4/28/04
McGinn & Gibb, PLLC
8321 Old Courthouse Road, Suite 200
Vienna, VA 22182-3817
(703) 761-4100
Customer No. 21254

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)



Docket Number (Optional)

NE-114-US/kwg

Application Number

10/649,833

Applicant(s)

Kazuhiro Nakajima

Filing Date

August 28, 2003

Group Art Unit

2817

PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		6,188,293 B1	02/13/2001	Miyagi et al.			
		4,473,762	09/25/1984	Iwahashi et al.			

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
		EP 0 731 560 A1	09/11/1996	European				
		EP 0 634 837 A2	01/18/1995	European				
		55-55621	04/23/1980	Japan			ABS	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		European Search Report dated March 26, 2004.
		"Voltage-Controlled Oscillator", IBM Technical Disclosure Bulletin, Vol. 33, No. 10A, March 1, 1991, pp. 286-288, XP000110044.

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.